

## REMARKS

Claims 9-18 remain pending in the present application. Applicants note with appreciation the indication that claims 10-14 include allowable subject matter.

Regarding the prior art rejections based on Millereau, this reference shows a device comprising a central unit 5 and electronic modules (M1, M2). The modules are connected via a power supply line 2 with a battery and via a data bus 7 with each other and the central unit. In the case of inactivity of the whole system, the central unit opens the switch 17 and the power supply to the modules is interrupted. A switch 18 is closed, so that some elements of the modules (esp. 14, 14') are supplied with current from the battery also in the state of inactivity. In a wake-up state (closing of switch 13) the elements 13 and 14 of the module generates a wake up signal which is transmitted to the central unit 5 via the data bus 7. The central unit closes switch 17 and provides power supply to the modules. Millereau teach to send a wake up signal via a data bus, and not via the power supply line as required by the claimed invention. Accordingly, withdrawal of the prior art rejections is requested.

It is respectfully submitted that the subject matter of the present application is new, non-obvious, and useful. Prompt consideration and allowance of the application are respectfully requested.

Respectfully submitted,

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